



Digital Electronics™

Final Examination

Parts A, B, & C

ANSWER KEY

Spring 2009

For Teacher Use ONLY

Part A – Multiple Choice Questions

Question #	Answer	DE Assessment Concepts
1	B	1 – Fundamentals (14%)
2	A	1 – Fundamentals
3	C	1 – Fundamentals
4	B	1 – Fundamentals
5	A	1 – Fundamentals
6	B	1 – Fundamentals
7	A	2 – Number Systems (8%)
8	B	2 – Number Systems
9	D	2 – Number Systems
10	B	3 – Gates (12%)
11	C	3 – Gates
12	D	3 – Gates
13	A	3 – Gates
14	B	3 – Gates
15	B	4 – Boolean Algebra (20%)
16	C	4 – Boolean Algebra
17	D	4 – Boolean Algebra
18	A	4 – Boolean Algebra
19	B	4 – Boolean Algebra
20	C	4 – Boolean Algebra
21	A	4 – Boolean Algebra
22	D	4 – Boolean Algebra
23	A	5 – Combinational Logic Circuit Design (14%)
24	D	5 – Combinational Logic Circuit Design
25	C	5 – Combinational Logic Circuit Design
26	A	5 – Combinational Logic Circuit Design
27	A	5 – Combinational Logic Circuit Design
28	A	6 – Adding (4%)
29	D	6 – Adding
30	D	7 – Flip Flops (14%)
31	B	7 – Flip Flops
32	C	7 – Flip Flops
33	A	7 – Flip Flops
34	B	7 – Flip Flops
35	A	7 – Flip Flops
36	C	8 – Shift Registers & Counters (8%)
37	D	8 – Shift Registers & Counters
38	C	8 – Shift Registers & Counters
39	C	9 – Families & Specifications (6%)
40	D	9 – Families & Specifications
Answer Breakdown: A-12; B-10; C-9; D-9		